Amendments to the Claims

This listing of claims will replace all prior versions and listings of the claims in the application:

1-12. (Cancelled)

13. (New) A receiver comprising:

(a) a first input for receiving a packetized input data stream comprised of multiplexed and compressed packets, each of said packets having at least header and payload data;

(b) a second input for receiving an analog signal;

(c) a decoder unit partitioning said packetized data stream to generate a video component and an audio component;

(d) a processor processing said analog signal to generate a digitized audio signal and a digitized video signal;

(e) first digital signal processing arrangement decompressing said video component of said packetized data stream, and digital signal processing said decomposed video component and said digitized video signal to generate a video output signal;

(f) second digital signal processing arrangement decompressing said audio component of said packetized data stream, and digital signal processing said decompressed audio component and said digitized audio signal to generate an audio output signal;

(g) a delay selectively delaying the processing of the digitized audio signal to synchronize an audible audio signal with a displayable video signal; and

(h) a converting arrangement transposing said video output signal to the displayable video signal and said audio output signal to the audible output signal.

14. (New) The receiver of claim 13 wherein said delay comprises an adjustable memory device.

15. (New) The receiver of claim 14 wherein said delay is connected to said second processing means.

16. (New) The receiver of claim 13 wherein said delay includes said partitioning means.

17. (New) The receiver of claim 13 wherein said second digital signal processing arrangement further comprises third audio processor for processing said audio output signal.

18. (New) The receiver of claim 17 wherein said third audio processor comprises a surround sound processor.

19. (New) The receiver of claim 13 wherein said first digital signal processing arrangement comprises a converter converting said digitized video signal having an interlace video format into a digitized video signal having a progressive scan format.

20. (New) A method for processing input signals having video and audio components, said method comprising:

receiving a packetized input data stream comprised of multiplexed and compressed packets, each of said packets having at least header and payload data;

receiving an analog signal;

partitioning said packetized data stream to generate a video component and an audio component;

processing said analog signal to generate a digitized video signal and a digitized audio signal;

decompressing said video component of said packetized data stream generate a decompressed video signal, and digital signal processing said decompressed video signal and said digitized video signal to generate a video output signal;

decompressing said audio component of said packetized data stream to generate a decompressed audio signal, and digital signal processing said

decompressed audio signal and said digitized audio signal to generate an audio output signal;

delaying the processing of the digitized audio signal to synchronize an audible audio signal with a displayable video signal; and

transposing said video output signal to the displayable video signal and said audio output signal to the audible output signal.

21. (New) The method of claim 20 wherein the step of delaying further comprising providing said audible audio signal to an adjustable memory device.

22. (New) The method of claim 21 further comprising the step of providing said audio output signal to a secondary audio processor.

23. (New) The method of claim 20 further comprising converting said digitized video signal into a progressive scan format.

24. (New) A receiver comprising:

a tuner receiving a packetized input data stream comprised of multiplexed and compressed packets, each of said packets having at least header and payload data;

an input for receiving an analog signal;

a processor processing said analog signal to generate a digitized audio signal and a digitized video signal;

a transport decoder unit partitioning said packetized data stream to generate a video component and an audio component;

a first digital signal arrangement decompressing said video component to generate decompressed video signal, wherein said first digital signal processing arrangement applies a first digital signal processing function to said decompressed video signal and said digitized video signal to produce a video output signal;

a second digital signal arrangement decompressing said audio component to generate decompressed audio signal, wherein said second digital signal processing

arrangement applies a second digital signal processing function to said decompressed audio signal and said digitized audio signal to generate an audio output signal;

a delay selectively delaying the processing of the digitized audio signal to synchronize an audible audio signal with a displayable video signal; and

a converting arrangement transposing said video output signal to the displayable video signal and said audio output signal to the audible output signal.